

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	25426	(logic with (FPGA pld))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 06:59
L2	246	I1 and (logic with correct\$3 with (error fault))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:00
L3	9558	(714/3 714/8 714/25 714/48 714/723 714/724 714/725 714/726 714/727 714/729 714/736 714/741 714/742 714/746 714/759 714/799 714/764 703/28).ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:00
L4	2	(external adj in adj circuit adj emulator)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:00
L5	16	(external adj ("in-circuit") adj emulator)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:00
L6	2076	(714/3 714/8 714/25).ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:00
L7	56	L6 and (redundan\$3 with logic)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:53
L8	700	(L3 or L4 or L5 or L7) and (pld fpga)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:49
L9	509	L8 and (logic with (FPGA pld))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:51
L10	227	I2 and (not L9)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:00
L11	13	I10 and (embed\$4 near3 (fpga pld))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:02
L12	12	I10 and (replac\$3 near3 logic)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:02
L13	24	I11 I12	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:47
L14	7234	(703/28 716/1 716/4 716/5 716/16 716/17 716/18).ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:48

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L15	1796	l14 and (pld fpga pga fp "pro-logic" (programmable adj logic))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:50
L16	1414	L15 and (logic with (FPGA pld "pro-logic" (programmable adj logic) pga fp))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 07:51
L17	56	L16 and (redundan\$3 with logic)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 08:44
L18	18	"20020066956" "20060117274" "20060225002" "20060179435" "20030014743" "20020173298" "20030052176" "20030099358" "200602248493" "20040034843" "20040183564" "20060101288" ("6334207" "6178541" "5852561" "6937493" "6766406" "7042899"). pn.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 09:13
L19	192	(error correct) and (hybrid with (fpga pld fp pla "pro-logic" (programmable adj logic))) and logic	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 08:55
L20	24	L19 and (redundan\$3 with logic)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 08:45
L21	82	(fault) and (hybrid with (fpga pld fp pla "pro-logic" (programmable adj logic))) and logic	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 08:55
L22	80	l21 and (not l20)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 08:55
L23	253050	(cohn-j\$ reynolds-c\$ ventrone-s\$ zuchowski-p\$).in. (ibm (international adj business))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 09:14
L24	31	l23 and (embedd\$3 near3 programmable).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 09:16
L25	1	l24 and (fault error correct\$3).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 09:16
L26	187	(embedd\$3 near3 programmable).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 09:16
L27	1	l25 and (fault error correct\$3).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 09:17

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L28	21	l26 and (fault error correct\$3).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 09:17
S1	1	10/709,754	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/06/27 11:35
S2	0	10/994,476	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/06/27 11:32
S3	2	(external adj in adj circuit adj emulator)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/06/27 11:35
S4	16	(external adj ("in-circuit") adj emulator)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/06/27 12:14
S5	2033	(714/3 714/8 714/25).ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/13 17:23
S6	54	S5 and (redundan\$3 with logic)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/06/27 12:15
S7	9544	(714/3 714/8 714/25 714/48 714/723 714/724 714/725 714/726 714/727 714/729 714/736 714/741 714/742 714/746 714/759 714/799 714/764 703/28).ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/13 17:26
S8	2	(external adj in adj circuit adj emulator)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/13 17:27
S9	16	(external adj ("in-circuit") adj emulator)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/13 17:27
S10	2071	(714/3 714/8 714/25).ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/13 17:27
S11	55	S10 and (redundan\$3 with logic)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/13 17:27
S12	698	(S7 or S8 or S9 or S11) and (pld fpga)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/13 17:27
S13	508	S12 and (logic with (FPGA pld))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 06:59
S14	18	S13 and (logic with correct\$3 with (error fault))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/08/14 06:59